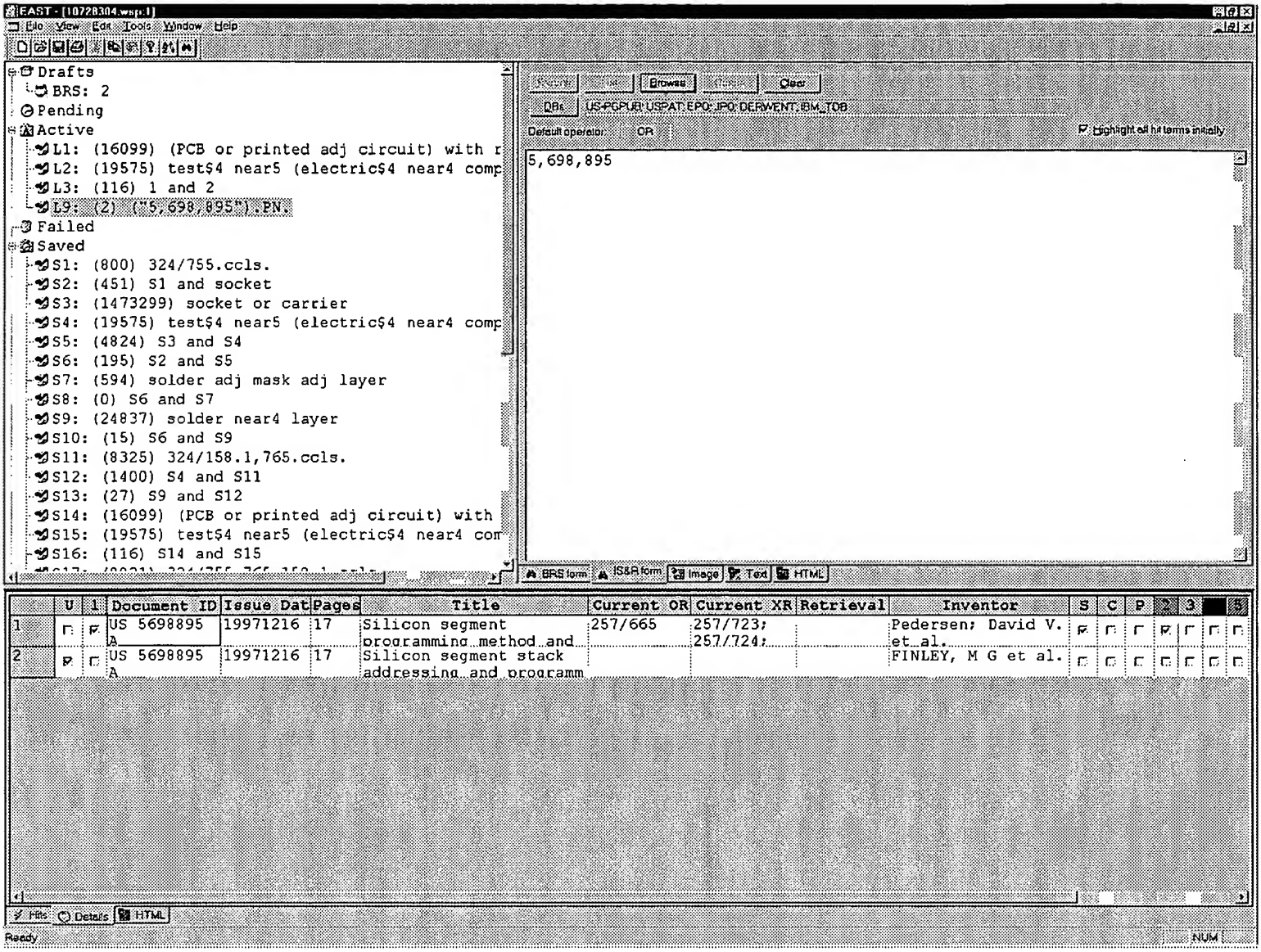
EAST - [10728304.wsp:1]
File View Edit Tools Window Help
Drafts
BRS: 2
Pending
Active
L1: (16099) (PCB or printed adj circuit) with r
L2: (19575) test$4 near5 (electric$4 near4 comp
L3: (116) 1 and 2
L9: (2) ("5,698,895").PN.
Failed
Saved
S1: (800) 324/755.ccls.
S2: (451) S1 and socket
S3: (1473299) socket or carrier
S4: (19575) test$4 near5 (electric$4 near4 comp
S5: (4824) S3 and S4
S6: (195) S2 and S5
S7: (594) solder adj mask adj layer
S8: (0) S6 and S7
S9: (24837) solder near4 layer
S10: (15) S6 and S9
S11: (8325) 324/158.1,765.ccls.
S12: (1400) S4 and S11
S13: (27) S9 and S12
S14: (16099) (PCB or printed adj circuit) with
S15: (19575) test$4 near5 (electric$4 near4 com
S16: (116) S14 and S15
Browse
Clear
DBs
US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
Default operator:
OR
Highlight all hit terms initially
5,698,895
BRS form
IS&R form
Image
Text
HTML
U
I
Document ID
Issue Dat
Pages
Title
Current OR
Current XR
Retrieval
Inventor
S
C
P
2
3
1
US 5698895 A
19971216
17
Silicon segment programming method and
257/665
257/723; 257/724;
Pedersen; David V. et al.
2
US 5698895 A
19971216
17
Silicon segment stack addressing and programm
FINLEY, M G et al.
Hits
Details
HTML
Ready
NUM